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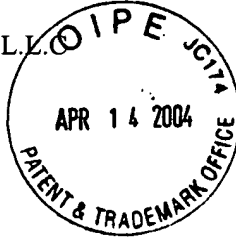
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7590

01/15/2004

RADER, FISHMAN & GRAUER, P.L.L.C.
 Suite 501
 1233 20th Street, N.W.
 Washington, DC 20036



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|--------------------|
| (Depositor's name) |
| (Signature) |
| (Date) |

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 09/964,540 | 09/28/2001 | Yukio Okuda | SON-2230 | 2656 |

TITLE OF INVENTION: METHOD AND APPARATUS OF DETERMINING DEFECT-FREE SEMICONDUCTOR INTEGRATED CIRCUIT

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1330 | \$300 | \$1630 | 04/15/2004 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|-------------|----------|----------------|
| KIM, PAUL L | 2857 | 702-064000 |

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1. **Rader, Fishman & Grauer**
 2. **PLLC**
Ronald P. Kananen
 3.

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Sony Corporation**JAPAN**Please check the appropriate assignee category or categories (will not be printed on the patent); ☒ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature)

(Date) **4/14/04****Ronald P. Kananen, Reg. No. 24,104**

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